ACKNOWLEDGEMENT RECEIPT

Electronic Version

Stylesheet Version v01

Title of Invention

Stacking Memory Chips Using Flat Lead-Frame with Breakaway Insertion Pins and Pin-to-Pin Bridges

Submision Type: Utility Patent Filing

Application Number:

10/707624 FES ID: 52843

Server Response:

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1623

First Named Applicant: Ren-Kang Chiou

Attorney Docket Number: ML-17

Timestamp: 2003-12-24 16:46:19 EDT

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File Listina:

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